9/1

Notice of Allowability	Application No.	Applicant(s)	
	10/706,597	MITSUHIRO ISHIGURO	
Notice of Allowability	Examiner	Art Unit	
	Andrew Q. Tran	2824	
The MAILING DATE of this communication appe All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RI of the Office or upon petition by the applicant. See 37 CFR 1.313	(OR REMAINS) CLOSED in this app or other appropriate communication GHTS. This application is subject to	olication. If not include will be mailed in due	ed course. THIS
1. This communication is responsive to Preliminary Amendment	ent filed November 12, 2003.		
2. X The allowed claim(s) is/are <u>1-9</u> .			
3. The drawings filed on 23 August 2004 are accepted by the Examiner.			
 4. Acknowledgment is made of a claim for foreign priority un a) All b) Some* c) None of the: 1. Certified copies of the priority documents have 2. Certified copies of the priority documents have 3. Copies of the certified copies of the priority documents 	been received. been received in Application No		tion from the
International Bureau (PCT Rule 17.2(a)).			
* Certified copies not received:			
Applicant has THREE MONTHS FROM THE "MAILING DATE" on noted below. Failure to timely comply will result in ABANDONM THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.	ENT of this application.	., .	
 A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient. 			
6. CORRECTED DRAWINGS (as "replacement sheets") must be submitted.			
(a) Including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached			
1) hereto or 2) to Paper No./Mail Date			
(b) ☐ including changes required by the attached Examiner's Paper No./Mail Date	Amendment / Comment or in the O	ffice action of	
Identifying indicia such as the application number (see 37 CFR 1. each sheet. Replacement sheet(s) should be labeled as such in the			back) of
7. DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.			
Attachment(s)			
1. Notice of References Cited (PTO-892)	5. Notice of Informal Pa	atent Application (PT0	D-152)
2. Notice of Draftperson's Patent Drawing Review (PTO-948)	6. Interview Summary		
 Information Disclosure Statements (PTO-1449 or PTO/SB/08/2003 	Paper No./Mail Date 8), 7. ⊠ Examiner's Amendm		
4. Examiner's Comment Regarding Requirement for Deposit	8. 🛛 Examiner's Stateme	nt of Reasons for Allo	wance
of Biological Material	9.	abr	
	c	ANDREW Q.TRAN	

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EXAMINER'S AMENDMENT

An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

The application has been amended as follows:

In the title:

Amend the current title as follows:

--Semiconductor memory with inter-block bit wires and inter-block ground wires--.

In the claims:

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In claim 1, line 13, after "bit" (first occurrence), insert --column--; line 19, after "ground" (both occurrence), insert --column--; line 21, after "ground" (both occurrence), insert --column--; line 22, change "lines" to --wires--; line 24, before "first", insert --a--; line 25, change "the" (second occurrence) to --an--; line 31, change "the" (first occurrence) to --an--; line 3, change "lines" to --wires--; line 3, before "select", add --column--; line 5, change "lines" to --wires--; line 3, after "ground" (second occurrence), add --column--; line 3, after "ground" (second occurrence), add --column--; line 5, change "line" to --wires--; line 5, change "line" to --wires--; line 6, change "the diffusion line" to --three diffusion wires--;
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In claim 9, line 15, change "bit line" to --bit wire--; and line 16, change "lines" to --wires--.

The changes have been made to overcome antecedent basis deficiencies and to correct typographical errors.

REASONS FOR ALLOWANCE

The following is an examiner's statement of reasons for allowance:

Independent claims 1 and 9 recite a semiconductor memory comprising, among other things, inter-block bit wires or inter-block ground wires, which features are absent from the prior art.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Andrew Q. Tran whose telephone number is (571) 272-1885. The examiner can normally be reached on Mon - Fri 8:30 AM - 5:30 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Richard T. Elms can be reached on (571) 272-1869. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

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at September 20, 2004